U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
	AA	6,295,299	9/25/2001	Haddock, et al.			
	AB	6,034,957	3/7/2000	Haddock, et al.			

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		None						X
								X

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	AC	Van Ess, D., *A Circular FIFO, PSoC Style*, **Cypress Microsystems, Application Note AN2036**, Rev. B, pp. 1-5, June 21, 2002.
	AD	Network Working Group, *RFC 1071 – Computing the Internet Checksum*, September 1988, *available at:* http://www.faqs.org/rfcs/rfc1071.html
	AE	Network Working Group, *RFC 1141 – Incremental Updating of the Internet Checksum*, January 1990, *available at:* http://www.faqs.org/rfcs/rfc1141.html
	AF	Network Working Group, *RFC 1624 – Computation of the Internet Checksum via Incremental Update*, May 1994, *available at:* http://www.faqs.org/rfcs/rfc1624.html
	AG	Netlogic Microsystems, Product Brief NSE5000GLQ, Copyright 2004, *available at:* http://www.netlogicmicro.com/datasheets/nse5000glq.html
	AH	Zhang, Z., *Recovery of Memory and Process in DSM Systems: HA Issue #1*, **Hewlett-Packard Co. HPL-2001-76**, March 30, 2001.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /X.W/

EXAMINER: /Xavier S. Wong/	DATE CONSIDERED: 10/07/2008
EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant	